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Chap. 2 2 VLSI Digital Signal Processing Systems • Textbook: - K.K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation, John Wiley, 1999

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Adaptive filters are often realized either as a set of program instructions running on an arithmetical processing device such as a Microprocessor or Digital Signal Processing (DSP) chip, or as a set of logic operations implemented in a field programmable gate array (FPGA) or in Very Large Scale Integrated Circuit (VLSI).

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1.1 Introduction - VLSI Digital Signal Processing Systems

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VLSI Digital Signal Processing Systems: Design and Implementation, Keshab K. Parhi, ISBN: 978-0471241867, Wiley, 1999. Suggested references The Design Warrior's Guide to FPGAs, Devices, Tools and Flows, Clive "Max" Maxfield, ISBN: 0750676043 Digital Signal Processing with Field Programmable Gate Arrays, Uwe Meyer-Baese, 3rd

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He joined IIT, Kharagpur as a faculty member in 1994, where he

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currently holds the position of a professor in Electronics and Electrical Communication Engg. The teaching and research interests of Prof. Chakraborty are in Digital and Adaptive Signal Processing, VLSI Signal Processing, Linear Algebra and Compressive Sensing.

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